

WHAT IS CLAIMED IS:

1. A plasma display panel comprising:

a first substrate and a second substrate provided with a predetermined gap therebetween, and disposed substantially parallel to each other;

5 a plurality of address electrodes formed on the first substrate;

a first dielectric layer formed on a front surface of the first substrate, covering the address electrodes;

a plurality of barrier ribs mounted on the first dielectric layer with a predetermined height to provide a discharge space;

10 a phosphor layer formed within the discharge space;

a plurality of discharge sustain electrodes provided on a front surface of the second substrate facing the first substrate, and disposed generally perpendicular to the address electrodes;

15 a second dielectric layer formed on the front surface of the second substrate, covering the discharge sustain electrodes; and

a passivation layer coated on the second dielectric layer, comprising MgO and dopant elements Si and Fe, wherein the Fe is provided in an amount ranging from 15 to 90 ppm.

2. The plasma display panel according to claim 1, wherein the passivation layer comprises Si in an amount ranging from 50 to 500 ppm.

20 3. The plasma display panel according to claim 2, wherein the passivation layer comprises Si in an amount ranging from 80 to 350 ppm.

4. The plasma display panel according to claim 1, wherein the passivation layer comprises Fe in an amount ranging from 20 to 70 ppm.

25 5. The plasma display panel according to claim 4, wherein the passivation layer comprises Si in an amount ranging from 80 to 350 ppm.